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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10026802	FILING DATE 12/27/2001	CLASS 999	SUBCLASS	GAU 2812	EXAMINER Im
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\*\*CONTINUING DATA VERIFIED: *Im*  
THIS APPLICATION IS A DIV OF 08/633,307 04/15/1996 PAT 6,335,541  
WHICH IS A DIV OF 08/329,644 10/25/1994 PAT 5,643,826

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\*\* FOREIGN APPLICATIONS VERIFIED: *Im*  
JAPAN 5-294633 10/29/1993  
JAPAN 5-303436 11/09/1993  
JAPAN 5-307206 11/12/1993

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 740756-2415
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's initials <i>Im</i>	
TITLE : Method for manufacturing a semiconductor device	

U.S. DEPT. OF COMM./PAT. & TM-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
ISSUE FEE				Total Claims	Print Claim for O.G.
Amount Due	Date Paid	Primary Examiner		DRAWING	
TERMINAL DISCLAIMER				Sheets Drwg.	Figs. Drwg.
		PREPARED FOR ISSUE		Application Examiner	
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